



RAYALASEEMA UNIVERSITY :: KURNOOL
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS

I & III SEMESTER TIME TABLE (2017-18) 09-08-2017

Day	10AM - 11AM	11AM - 12Noon	12Noon - 1PM	1PM - 2PM	2PM - 3PM	3PM – 4PM	4PM-5PM	
Mon	DHK-103	KTA-102	KDD-104	L U N C H . . B R E A K	VMD-105	LAB- I Batch-1– Analog & digital -DHK		
	KDD-302	VMD-305	DHK-304		KTA-301	Lab-II Batch-II - MATLab - VMD		
Tue	KDD-104	DHK-103	VMD-105		KDD-101	Lab-V- Batch-1- Communication-KDD		
	VMD-305	KTA-303	KDD-302		VMD-301	-DO-		
Wed	DHK-103	VMD-105	KTA-102		-DO-		-DO-	
	KTA-303	KDD-302	DHK-304		-DO-		-DO-	
Thu	KTA-102	KDD-104	DHK-101		-DO-		-DO-	
	DHK-304	VMD-301	KTA-303		-DO-		-DO-	
Fri	KDD-104	DHK-101	KTA-102		-DO-		-DO-	
	KTA-303	VMD-305	KDD-302		-DO-		-DO-	
Sat	VMD-105	KDD-101	DHK-103		SEMINAR		SEMINAR	
	DHK-304	KTA-301	VMD-305		SEMINAR		SEMINAR	

I-Semister

Paper 101 : Semiconductor Devices & Network theory
Paper 102 : Computer Organization
Paper 103 : Analog Integrated Circuits and Applications
Paper 104 : Signals and Systems
Paper 105 : Electronics Instrumentation and measurement
Lab-I: Analog Electronics
Lab-II: signal and Systems using MATLAB

III- Semester

Paper 301 : Microprocessor and Embedded systems
Paper 302 : Analog and Digital Communication
Paper 303 : Data Communications
Paper 304 : Cellular and Mobile Communications
Paper 305: Electronics Instrumentation and measurement
Lab-V: Communication lab
Lab-VI: Embedded system

Faculty

Sri Dr. V. Mahammad Dada(**VMD**)
Sri Dr. K. Tanveer Alam (**KTA**)
Sri Dr. D. Hanumesh Kumar (**DHK**)
Sri K. Dharani Dhar (**KDD**)

(Handwritten Signature)
Signature of the Head of the Department
CO-ORDINATOR
 Dept. of Electronics & Communication
 Rayalaseema University